

398	137	Subclass	ISSUE CLASSIFICATION
		Class	

**PATENT NUMBER**

**U.S. UTILITY Patent Application**

<b>O.I.P.E.</b> AK SCANNED NO 4 Q.A. <i>[Signature]</i>	<b>PATENT DATE</b> [Blank]
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APPLICATION NO. 09/777917	CONT/PRIOR	CLASS 35-9	SUBCLASS <i>157</i>	ART UNIT 2633	EXAMINER <i>L. Lee</i>
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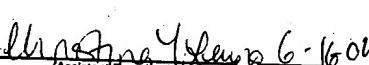
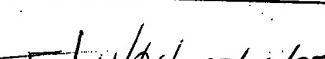
Lewis Aronson  
Stephen Hosking

## Integrated memory mapped controller circuit for fiber optics transceiver

THE

## **ISSUING CLASSIFICATION**

Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TER <input type="checkbox"/> DISC	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term subsequent has been dis				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term not extend t of U.S Pater	 Jason Chan (Assistant Examiner) (Date)			 5/4/04 07/06/05	
<input type="checkbox"/> The term this patent i	 JASON CHAN SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2600 (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
				\$ 70	
				130.00	
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